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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Tatsuya Arao et al.

Serial No. : 10/622,584

Filed : July 21, 2003

Title : METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE

Art Unit : 2815

Examiner : N. Drew Richards

Confirmation No.: 4703

Notice of Allowance Date: April 13, 2005

**MAIL STOP ISSUE FEE**

Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

**RESPONSE TO NOTICE OF ALLOWANCE**

In response to the Notice of Allowance mailed April 13, 2005, enclosed are a completed issue fee transmittal form PTOL-85b and a check for \$1730 for the required issue fee and publication fee, including patent copies.

Please apply any additional charges or credits to our Deposit Account No. 06-1050.

Respectfully submitted,

Date: July 13, 2005

  
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